Frequency Shift Acceleration Control for Anti-islanding of a Distributed **Generator**

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Overview

- 1. Introduction
- 2. Frequency Shift Acceleration Control
- 3. Design of Acceleration Gain
- 4. Simulation Results
- 5. Experimental Results
- 6. Conclusion

Introduction

- **E** Islanding
- Anti-islanding Methods
	- Passive
	- Active
- **Proposed Algorithm**
	- DQ control
	- Small signal analysis
	- Simulation & Experiment

Islanding

Non-Detection Zone (NDZ)

NDZ Impacts Islanding Detection

Anti-islanding Methods

FSAC

Islanding Condition \blacksquare

Key Idea

- Power controller of DG inverters

Design of Acceleration Gain

¾ When *islanded*, **large** enough to destabilize system *Small Signal Analysis*

 \triangleright When *grid-tied*, **small** enough to keep ΔQ beyond the limit

Frequency Step Response

- Lower Limit by Small Signal Analysis

$$
\left(Q_{inv}-Q^*\right)\left(K_p+\frac{K_i}{s}\right)+\left(\omega\frac{\omega_f}{s+\omega_f}-\omega_0\right)K_{pf}=i_d^*
$$

$$
\left(K_P + \frac{K_i}{s}\right)\Delta Q_{inv} + K_{pf} \frac{\omega_f}{s + \omega_f} \Delta \omega = \Delta i_d
$$

$$
s^{2} + \left[\frac{e_{q} \left\{ 2 + 3e_{q} \left(K_{p} + \frac{K_{i}}{\omega_{f}} \right) \right\} \left(\frac{Q_{f}}{\omega_{0} R} \right) - K_{pf}}{2 e_{q} \left(\frac{Q_{f}}{\omega_{0} R} \right) \left(1 + \frac{3}{2} e_{q} K_{p} \right)} \right] \omega_{f} s + \frac{3}{2} e_{q} \left(\frac{K_{i} \omega_{f}}{1 + \frac{3}{2} e_{q} K_{p}} \right) = 0
$$

For the islanded system to be unstable

$$
K_{pf} > \left\{ 2 + 3e_q \left(K_p + \frac{K_i}{\omega_f} \right) \right\} \cdot \left(\frac{Q_f}{\omega_0} \right) \left(\frac{e_q}{R} \right)
$$

$$
K_a > \left\{ 2 + 3\sqrt{2}V_n \left(K_p + \frac{K_i}{\omega_f} \right) \right\} \cdot \left(\frac{Q_f}{\omega_0} \right)
$$

 V_n : Inverter terminal voltage, ω_i Q_f : Quality factor, ω_0

 $\omega_{\rm f}$: Measuring frequency ω_0 : Nominal frequency

FSAC eliminates real power dependency of control gain !!

• Upper Limit by Freq. Step Response

$$
\Delta i_d \left(s \right) = (K_p + \frac{K_i}{s}) \Delta Q_{inv}(s) + K_{pf} \Delta \omega(s)
$$

$$
\left|\Delta Q_{inv}(s)\right| = \frac{K_{pf}}{K_p + 2/(3e_q) + K_i / s} \left|\frac{\Delta \omega}{s}\right|
$$

$$
\Delta Q_{inv}(t) = \frac{K_{pf}}{(K_p + 2/(3e_q)})\Delta \omega \bigg[\exp[st] \bigg]
$$

Maximum Q disturbance due to frequency step change

$$
\Delta Q_{\text{max}} > \frac{K_{\text{pf}}}{K_{\text{p}} + 2/(3e_{q})} |\Delta \omega_{\text{max}}|
$$

$$
\eta_{\text{preset}} = \frac{\Delta Q_{\text{max}}}{P_{\text{inv}}}
$$

$$
K_{\text{pf}} < \left(1 + \frac{3}{2} e_q K_p \right) \frac{\eta_{\text{preset}}}{|\Delta \omega_{\text{max}}|} \left(\frac{i}{q}\right)
$$

$$
K_a < \left(1 + \frac{3\sqrt{2}}{2} V_n K_p \right) \frac{1}{|\Delta \omega_{\text{max}}|} \cdot \eta_{\text{preset}}
$$

Simulation Results

- **E.** Simulation conditions
	- $P_{inv} = P_{load} = 20$ kW, $Q_{inv} = Q_{load} = 0$ kVar
	- Detection condition (IEEE P1547)
		- $-$ Voltage : 110% $>$ or $<$ 88%
		- Frequency : 60.5 Hz > or < 59.3 Hz
	- R-L-C Load (IEEE 929 & UL 1741) Quality factor $Q_f = 2.5 \rightarrow Q_L$ & $Q_C = 2.5 \times P_{inv}$
	- Calculated Range of *K^a* : *0.076* **<** *K^a* **< 0.3**
	- ˤ**preset = 0.1**

Without FSAC

With FSAC (K_a = 0.15)

Frequency Variations with Different gains of K_a

Calculated & Simulated results for Lower Limit of *K^a*

Harmonic Spectrum

Experimental Results

Before FSAC Implementation

After FSAC Implementation $(K_a = 0.1)$

Frequency with FSAC $(K_a = 0.057)$

Lower limit of *K^a* :

Calculation/simulation/experiment = 0.076/0.078/0.057

 \rightarrow Acceptable

Conclusion

- \triangleright Based on dq control and positive feedback
- ¾ *Pinv* dependency of control gain removed
- \triangleright Design method and criteria suggested
- \triangleright FSAC enables
	- Zero NDZ possible
	- Minimizing impact on power quality
	- Easy implementation